U.S. UTILITY Patent Application

Jc971 U.S. PTO
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02/05/02

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	2815	EXAMINER
10066542	02/05/2002	257	164	2814		

APPLICANTS: Nakamura Osamu; Kajiwara Masayuki; Koezuka Junichi; Yamazaki Shunpei; Kuwabara Hideaki;

CONTINUING DATA VERIFIED:

FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-034294 02/09/2001

PG-PUB	DO NOT PUBLISH ☐		RESCIND ☐	

		ATTORNEY DOCKET NO
Foreign priority claimed	☐ yes ☐ no	
35 USC 119 conditions met	☐ yes ☐ no	740756-2431
Verified and Acknowledged Examiners's intials		

TITLE : Semiconductor device and method for manufacturing the same

U.S.DEPT. OF COMM /PAT.& TM-PTO-436L(Rev 12-94)

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
	Assistant Examiner	Total Claims	Print Claim for O.G.
ISSUE FEE		**DRAWING**	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
	Primary Examiner		
☐ TERMINAL DISCLAIMER	PREPARED FOR ISSUE	Application Examiner	

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